

FIG. 1

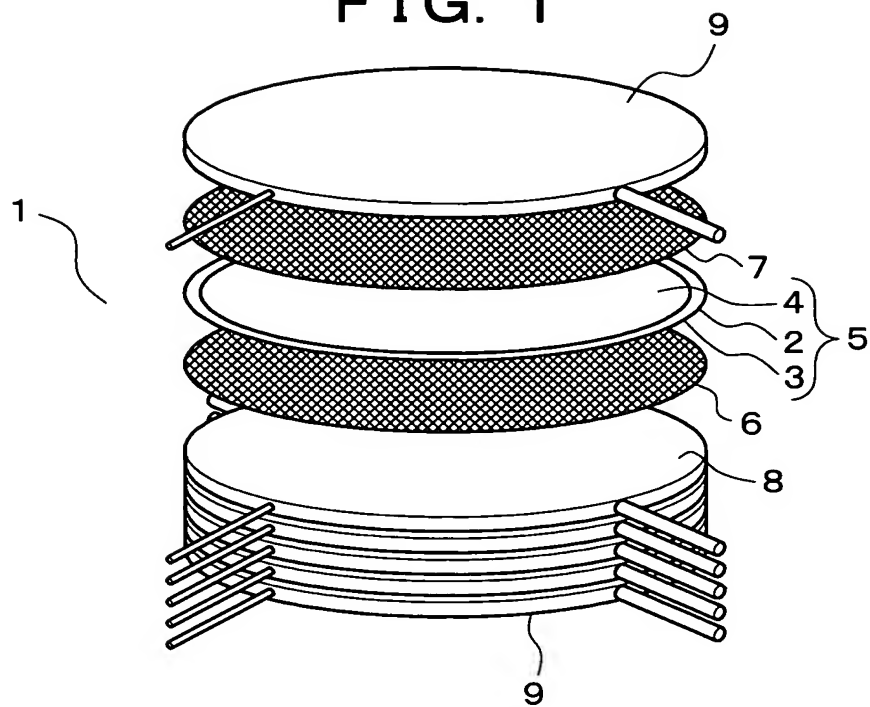


FIG. 2

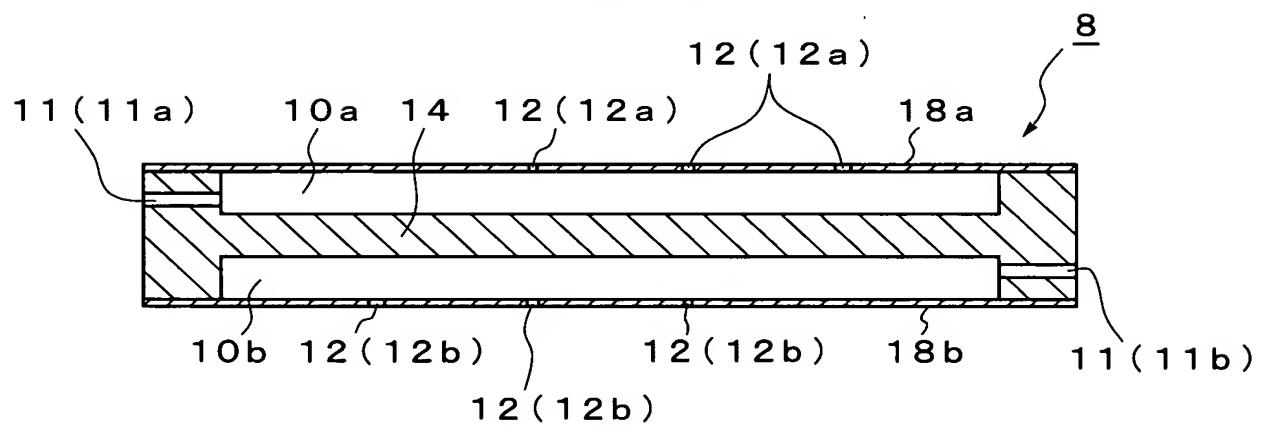


FIG. 3

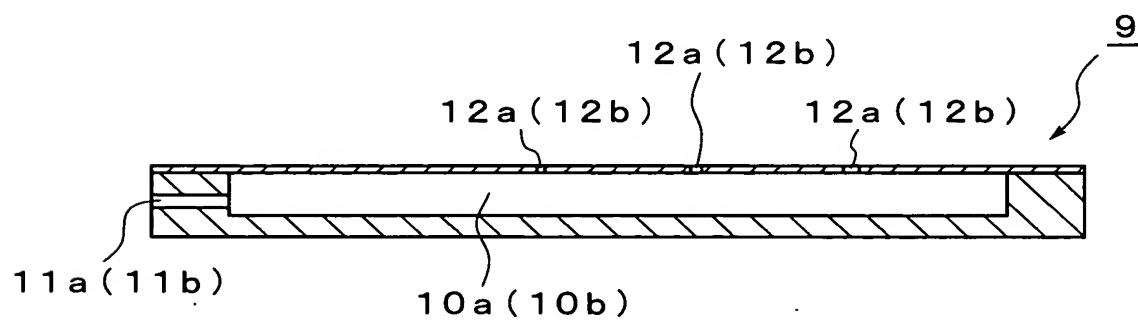


FIG. 4

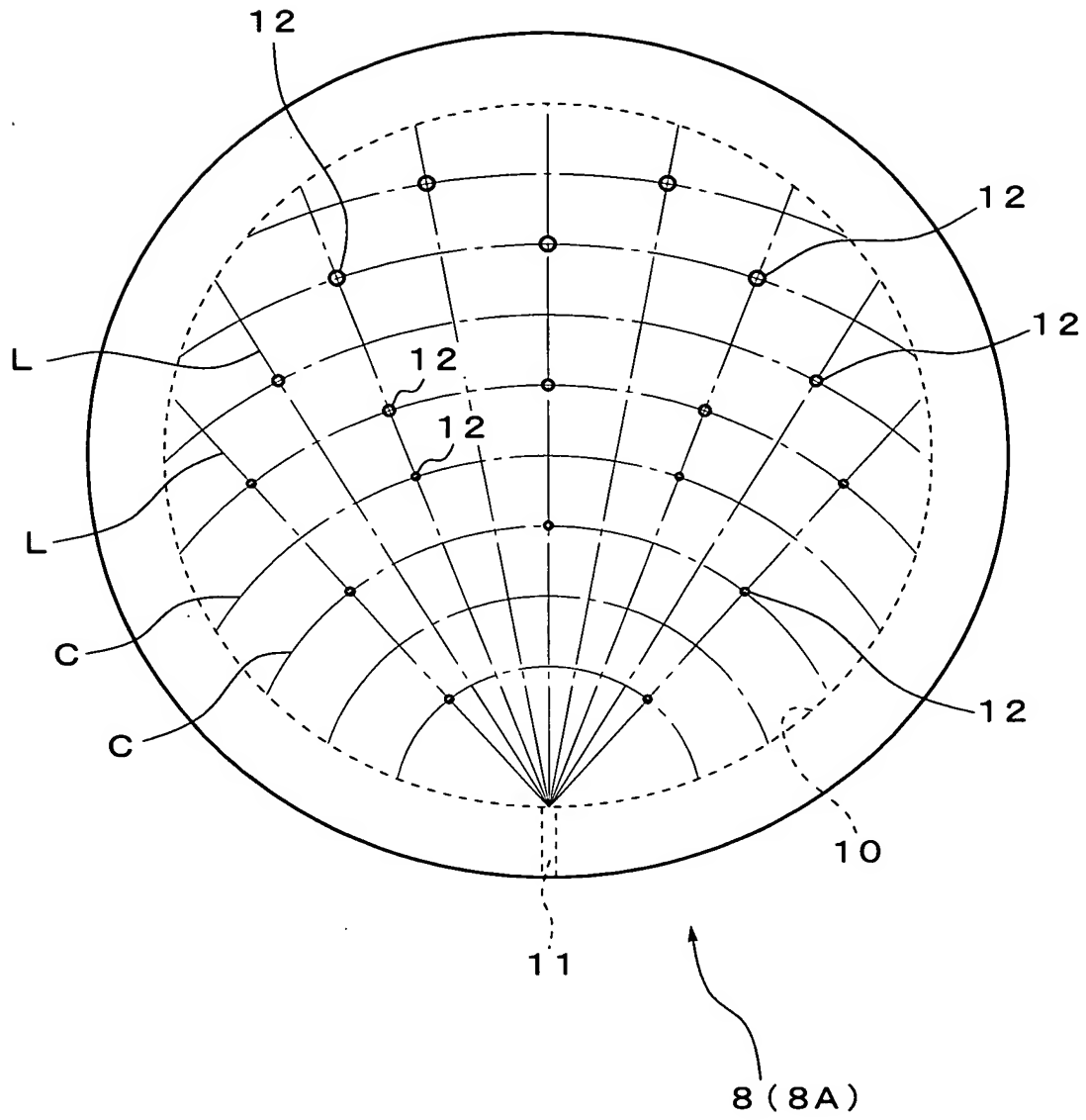


FIG. 5

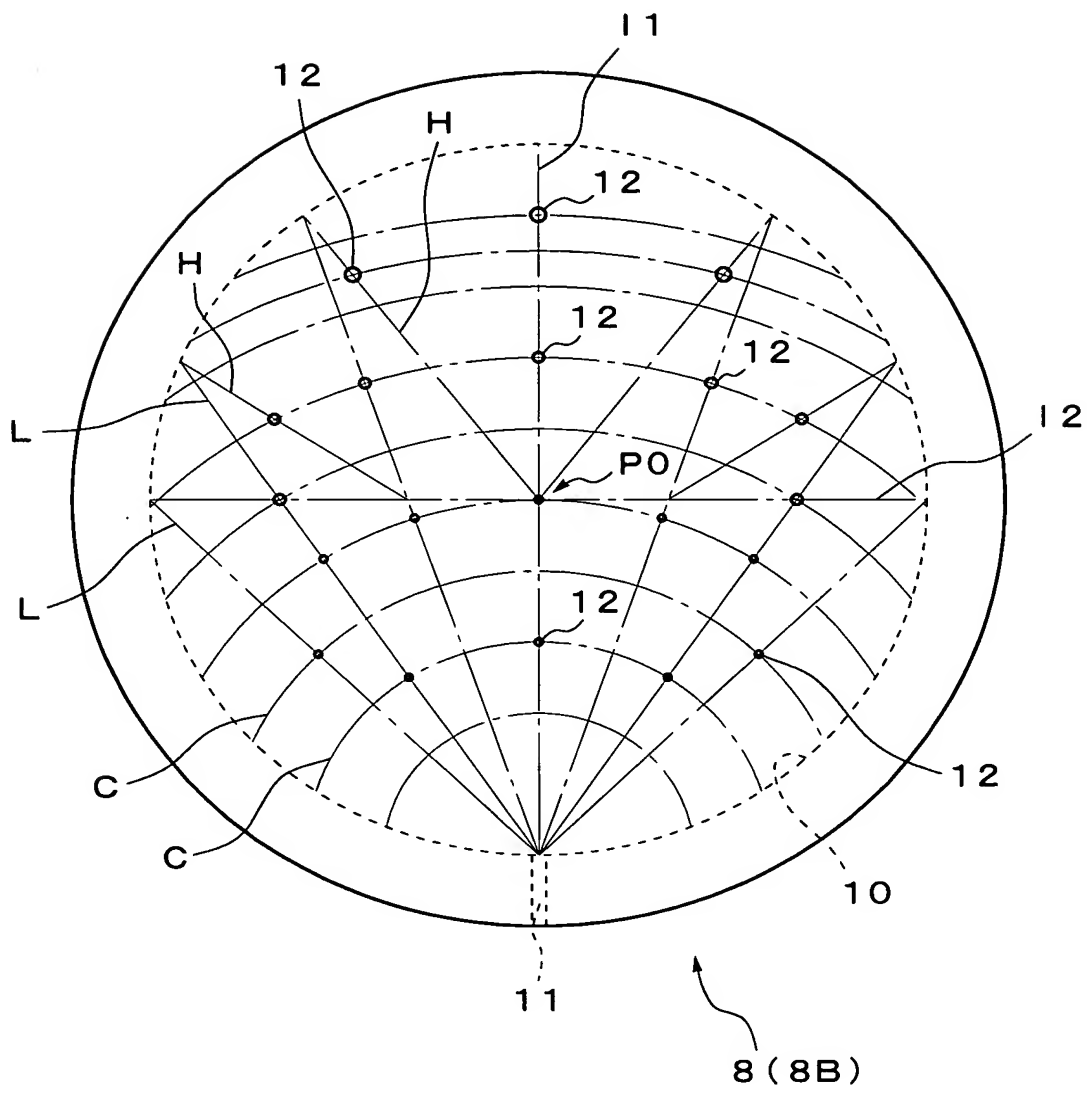


FIG. 6

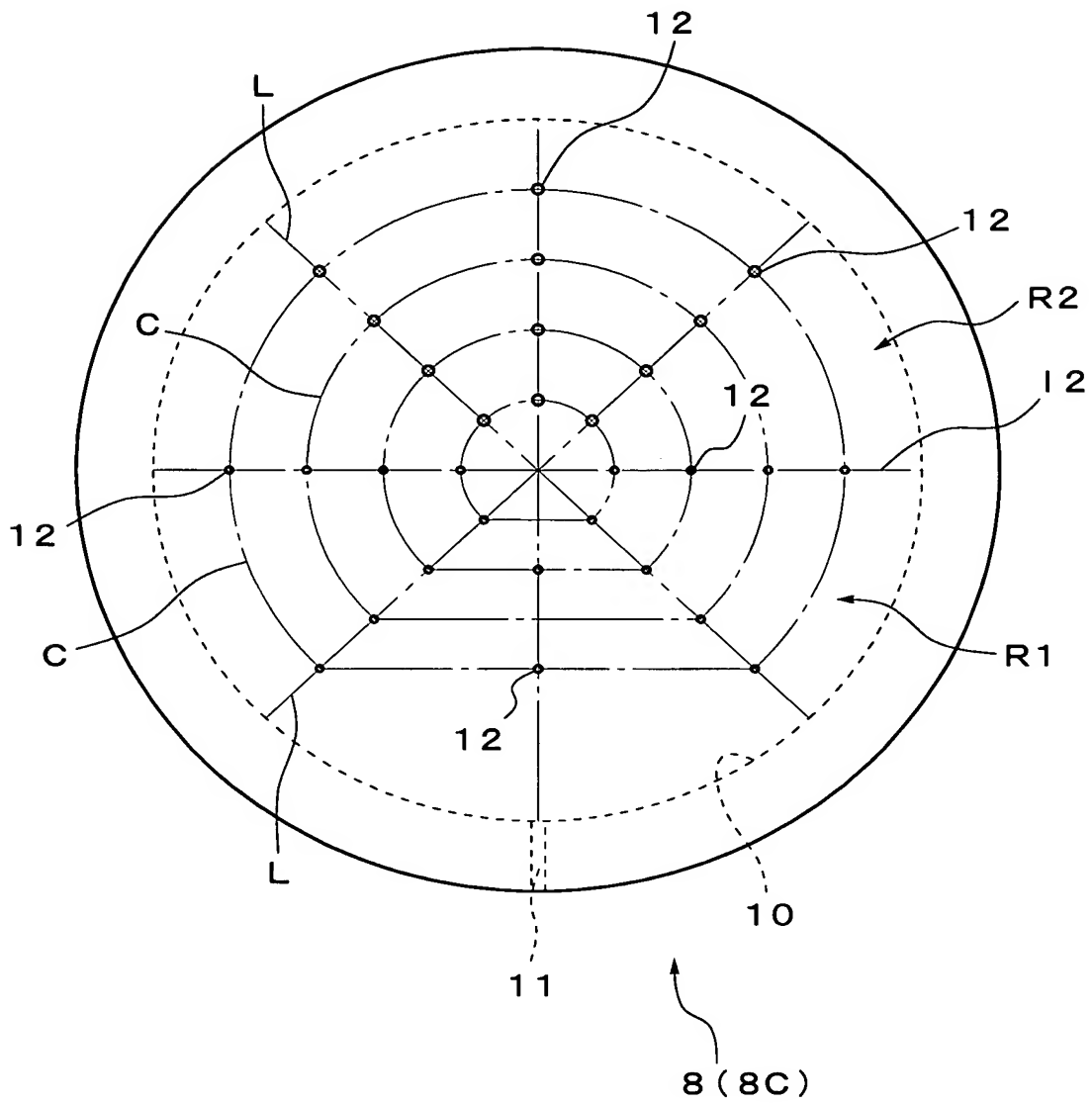


FIG. 7

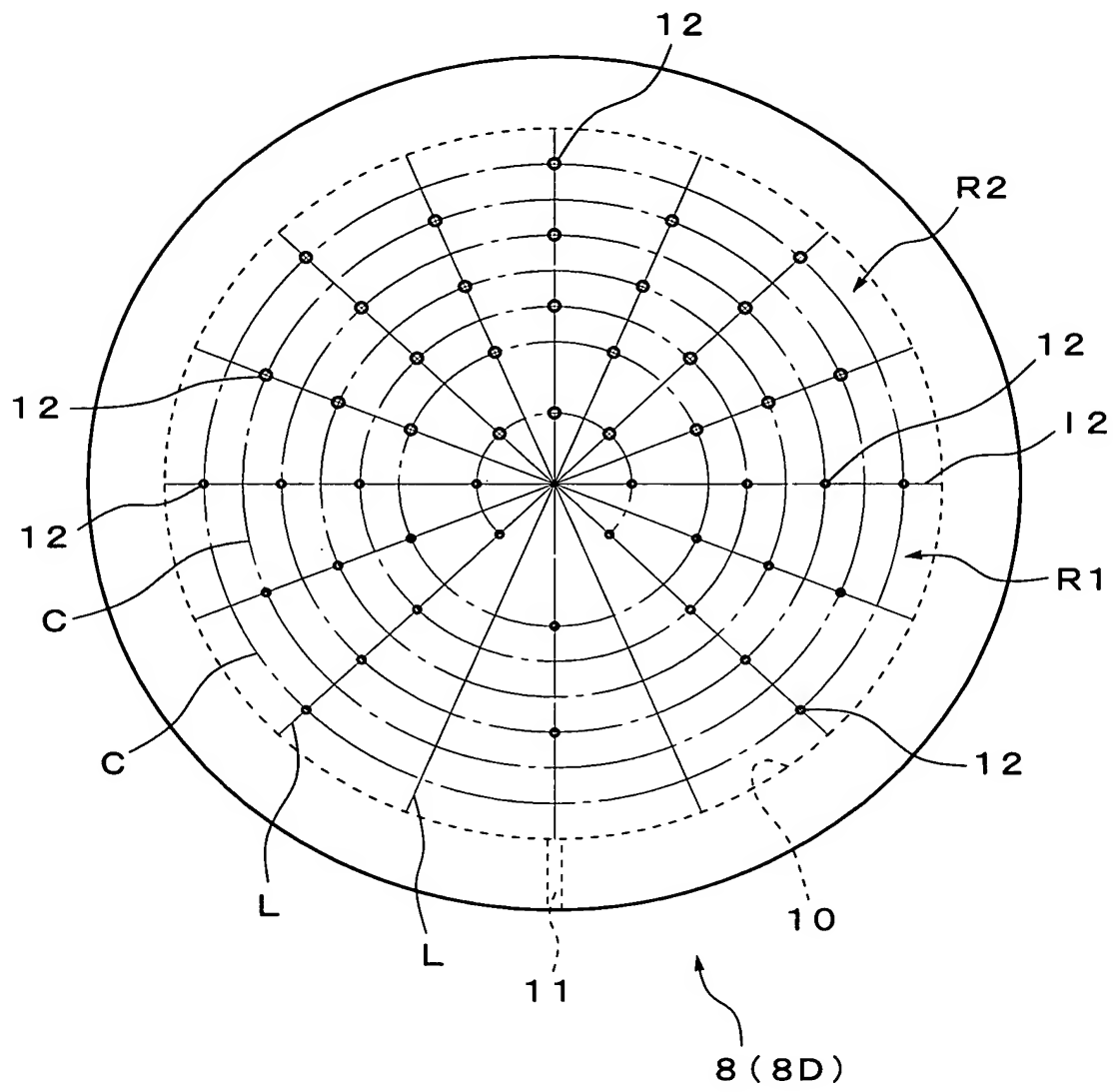


FIG. 8

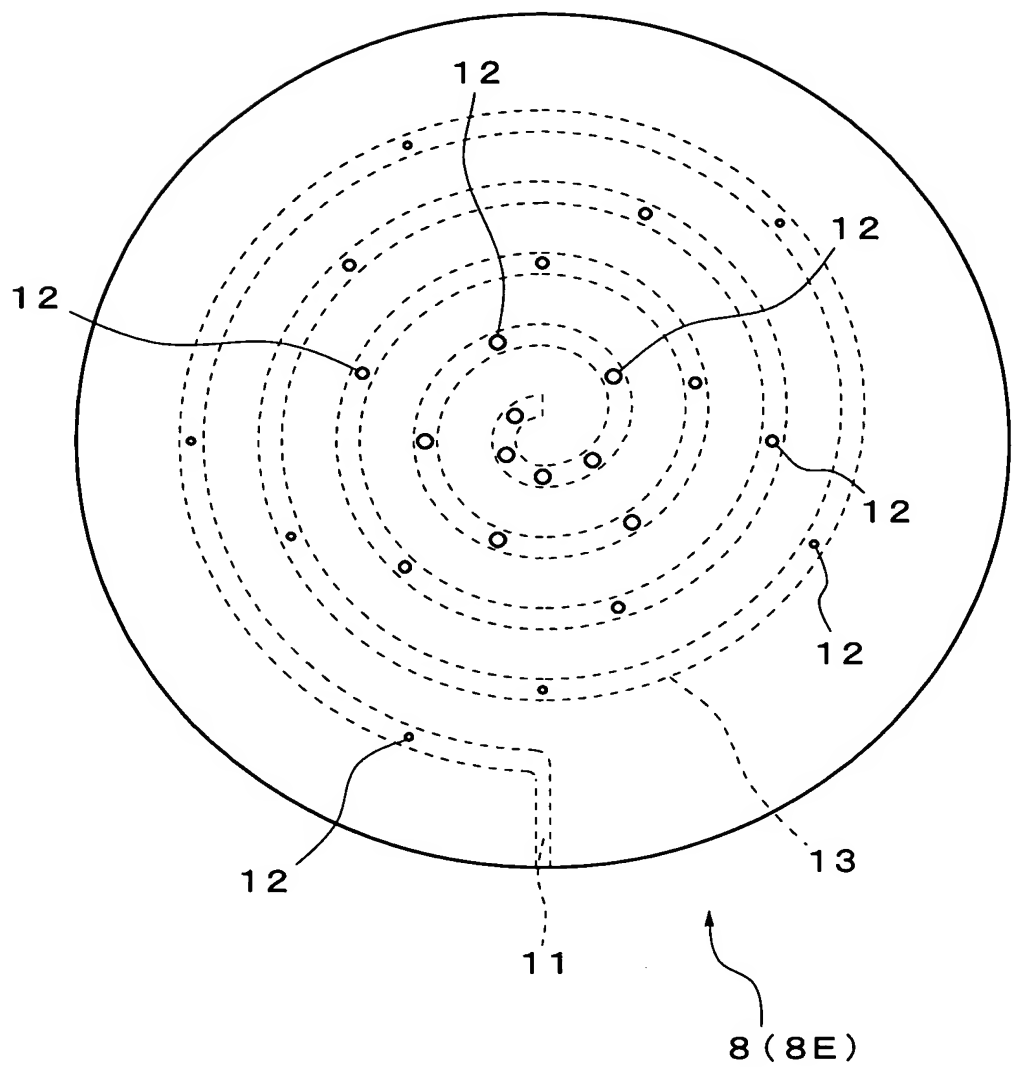


FIG. 9

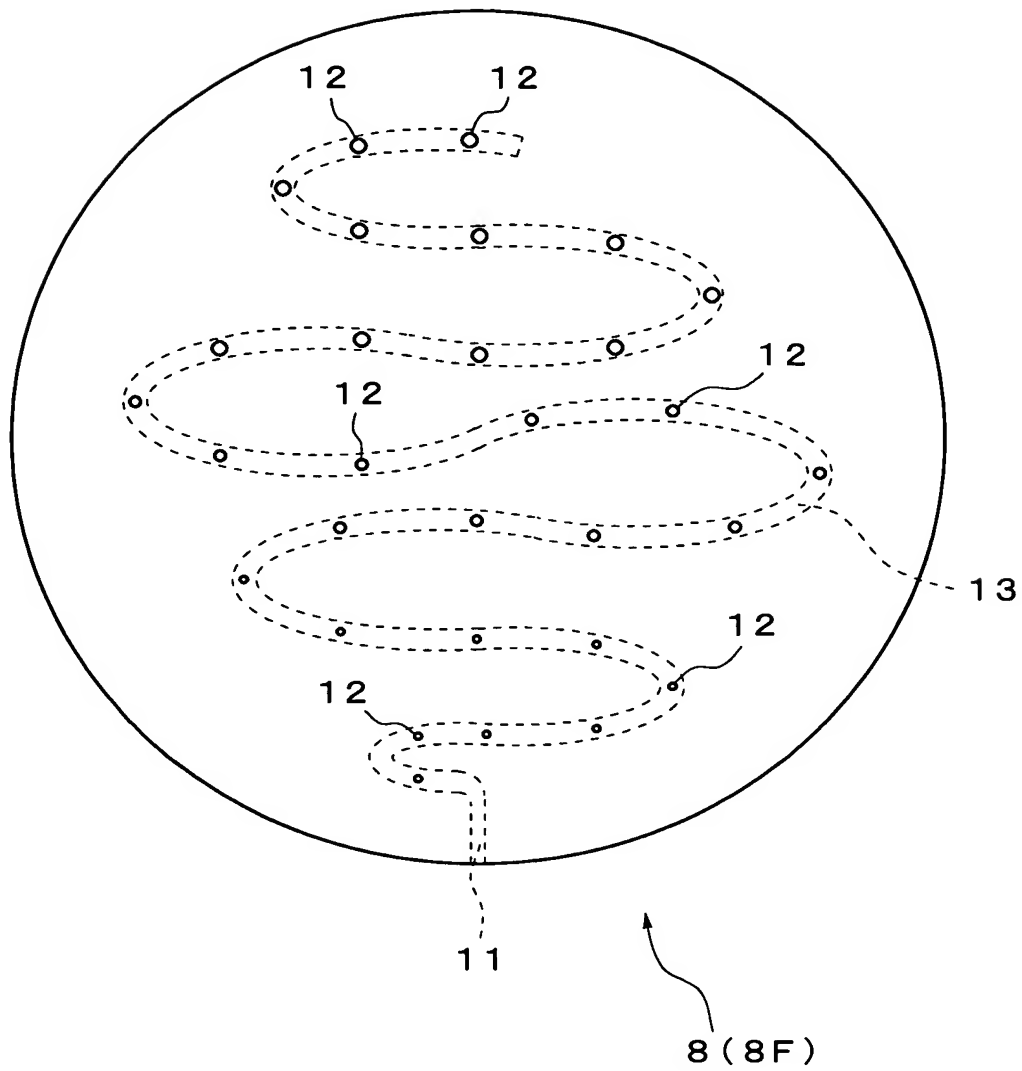
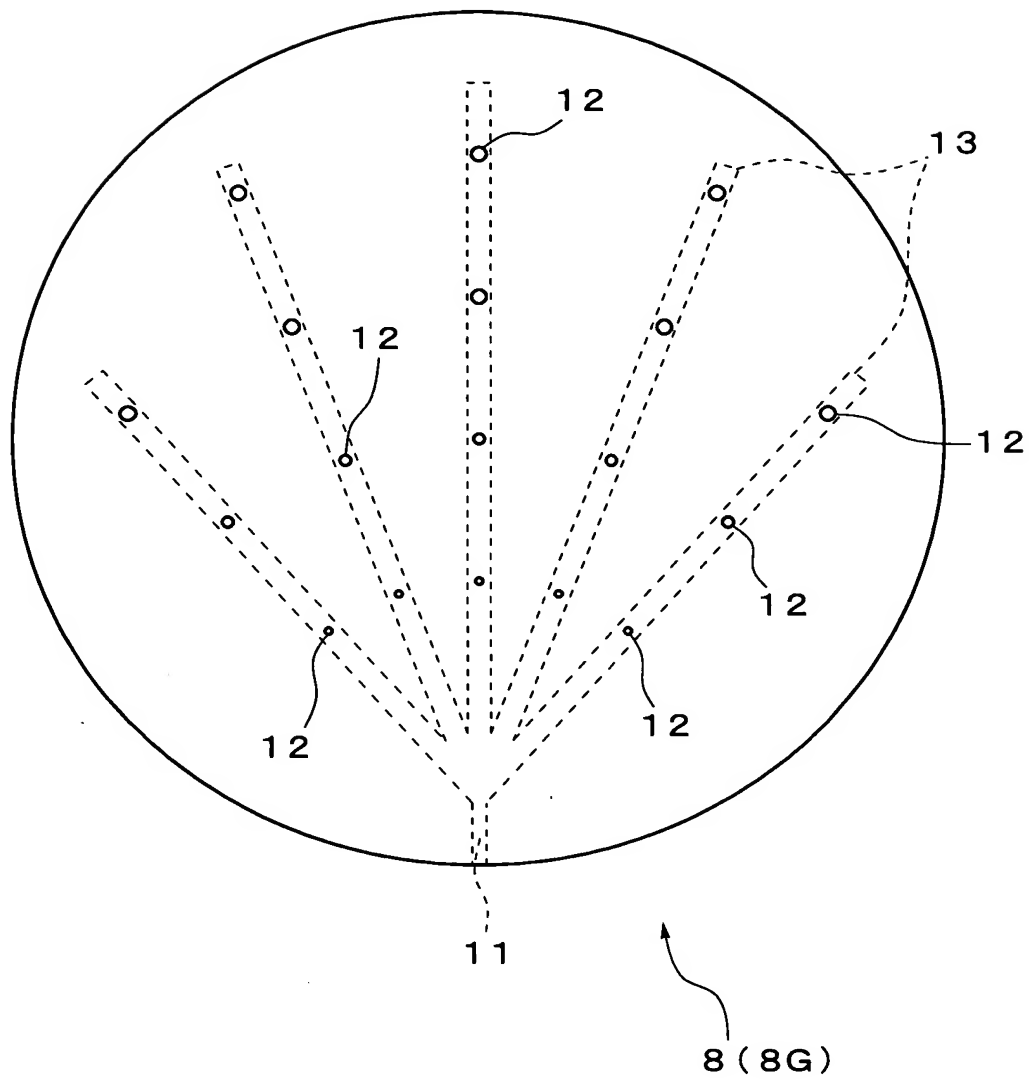


FIG. 10





A detailed cross-sectional view of a multi-layered electronic device 1. The device consists of a central stack of alternating layers: a top layer 9, followed by a series of layers 7, 6, and 5. Layer 5 is a thin, patterned layer that is electrically connected to a series of horizontal conductive lines 20a and 20b. These lines are embedded within a matrix of layers 6 and 7. The entire stack is bounded by side walls 31 and 32. The bottom layer is also labeled 9. The device is shown in a perspective view, with a wavy arrow 1 indicating the cross-section.

FIG. 12

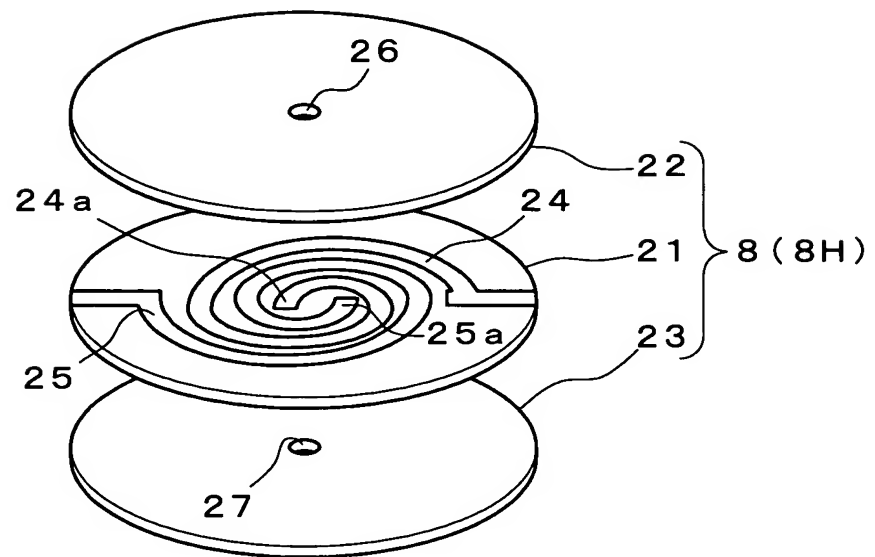


FIG. 13

